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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/603,446	06/25/2003	Mark W. Jennion	TN317	4372
7590 11/18/2004			EXAMINER	
Unisys Corporation Attn: Michael B. Atlass			WALLING, MEAGAN S	
Unisys Way, MS/E8-114			ART UNIT	, PAPER NUMBER
Blue Bell, PA 19424-0001			2863	
		DATE MAILED: 11/19/2004		

Please find below and/or attached an Office communication concerning this application or proceeding.

		EV.				
	Application No.	Applicant(s)				
	10/603,446	JENNION ET AL.				
Office Action Summary	Examiner	Art Unit				
	Meagan S Walling	2863				
The MAILING DATE of this communication appears on the cover sheet with the correspondence address Period for Reply						
A SHORTENED STATUTORY PERIOD FOR REPL THE MAILING DATE OF THIS COMMUNICATION. - Extensions of time may be available under the provisions of 37 CFR 1. after SIX (6) MONTHS from the mailing date of this communication. - If the period for reply specified above is less than thirty (30) days, a replif in No period for reply is specified above, the maximum statutory period. - Failure to reply within the set or extended period for reply will, by statuth Any reply received by the Office later than three months after the mailing earned patent term adjustment. See 37 CFR 1.704(b).	136(a). In no event, however, may a reply be ly within the statutory minimum of thirty (30) o will apply and will expire SIX (6) MONTHS fro e. cause the application to become ABANDO	timely filed ays will be considered timely. In the mailing date of this communication. NED (35 U.S.C. § 133).				
Status						
 Responsive to communication(s) filed on <u>25 June 2003</u>. This action is FINAL. 2b) This action is non-final. Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under <i>Ex parte Quayle</i>, 1935 C.D. 11, 453 O.G. 213. 						
Disposition of Claims						
4) Claim(s) 1-25 is/are pending in the application 4a) Of the above claim(s) is/are withdra 5) Claim(s) 1-21 is/are allowed. 6) Claim(s) 22 and 23 is/are rejected. 7) Claim(s) 24 and 25 is/are objected to. 8) Claim(s) are subject to restriction and/	awn from consideration.					
Application Papers						
10) ☐ The specification is objected to by the Examiner. 10) ☐ The drawing(s) filed on (25 03) is/are: a) ☐ accepted or b) ☐ objected to by the Examiner. Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a). Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d). 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.						
Priority under 35 U.S.C. § 119						
12) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f). a) All b) Some * c) None of: 1. Certified copies of the priority documents have been received. 2. Certified copies of the priority documents have been received in Application No 3. Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)). * See the attached detailed Office action for a list of the certified copies not received.						
Attachment(s) 1) Notice of References Cited (PTO-892) 2) Notice of Draftsperson's Patent Drawing Review (PTO-948) 3) Information Disclosure Statement(s) (PTO-1449 or PTO/SB/0 Paper No(s)/Mail Date	4) Interview Sumn Paper No(s)/Ma 5) Notice of Inform 6) Other:	nary (PTO-413) il Date nal Patent Application (PTO-152)				

DETAILED ACTION

Claim Objections

1. Claim 25 is objected to because it recites the limitation "said subcontractor" in claim 22.

There is insufficient antecedent basis for this limitation in the claim.

Claim Rejections - 35 USC § 102

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

- (e) the invention was described in (1) an application for patent, published under section 122(b), by another filed in the United States before the invention by the applicant for patent or (2) a patent granted on an application for patent by another filed in the United States before the invention by the applicant for patent, except that an international application filed under the treaty defined in section 351(a) shall have the effects for purposes of this subsection of an application filed in the United States only if the international application designated the United States and was published under Article 21(2) of such treaty in the English language.
- 2. Claims 22 and 23 are rejected under 35 U.S.C. 102(e) as being anticipated by Gorodetsky et al. (US 2002/0170000).

Regarding claim 22, Gorodetsky et al. teaches providing software data useful to test the non-identical models of circuit boards to a computer facility associated with the test apparatus (see paragraph 29); determining which model of the non-identical circuit board is to be tested (see paragraph 35); electrically connecting a one of the models of the non-identical circuit bards to be tested to the test apparatus (see paragraph 27); applying the appropriate test software for the model to the circuit board being tested to the computer facility based on the determining step (see paragraph 35); and running the appropriate test software for the model (see paragraph 39).

Regarding claim 23, Gorodetsky et al. teaches producing a receipt of likely faults to be associated physically with the circuit board being tested (see paragraph 52).

Allowable Subject Matter

3. Claim 24 is objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.

The following is a statement of reasons for the indication of allowable subject matter:

The primary reason for the indication of allowability of claim 24 is the inclusion of the limitation of delivery of the software to a subcontractor for use in the computer associated with the test apparatus in the subcontractor's facilities, and by the subcontractor formatting test vectors in the software into a form suitable for use in the circuit board model. It is this limitation in the claimed combination that has not been found, taught, or suggested in the prior art that makes these claims allowable.

4. Claims 1-21 are allowed.

The following is an examiner's statement of reasons for allowance: The primary reason for the allowance of claim 1 is the inclusion of the limitation of establishing a tester on-site with a third-party manufacturer/assembler/tester, said on-site tester having a connector mechanism for connection to each of said variety of circuit boards, translating a high-level language description file containing a high level language description of hardware for one of said variety of circuit boards into a low level vector format file, providing said low level vector format file for said one of said variety of circuit boards to said third-party manufacturer/assembler/tester, wherein said low level vector format file contains a test string for said one of said variety of circuit boards be tested, and wherein from said low level vector format test strings said third-party

Art Unit: 2863

manufacturer/assembler/tester can establish a set of test vectors to use in performing a scan test

on substantially said each one of said variety of circuit boards in said on-site tester. It is this

limitation in the claimed combination that has not been found, taught, or suggested in the prior

art that makes these claims allowable.

Any comments considered necessary by applicant must be submitted no later than the

payment of the issue fee and, to avoid processing delays, should preferably accompany the issue

fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for

Allowance."

Conclusion

Any inquiry concerning this communication or earlier communications from the

examiner should be directed to Meagan S Walling whose telephone number is (571) 272-2283.

The examiner can normally be reached on Monday through Friday 8:30 AM to 5 PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's

supervisor, John Barlow can be reached on (571) 272-2269. The fax phone number for the

organization where this application or proceeding is assigned is 703-872-9306.

Application/Control Number: 10/603,446

Art Unit: 2863

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

msw

John Barlow Supervisory Patent Examiner Technology Center 2800

Page 5